



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:

Sitaram YADAVALLI, et al.

Serial No.: 09/531,910

Group Art Unit: 2123

Filed: March 20, 2000

Examiner: H. Day

FOR: METHOD AND APPARATUS FOR MODELING AND
CIRCUITS WITH ASYNCHRONOUS BEHAVIOR

PROPOSED CHANGES TO THE DRAWINGS

Commissioner for Patents
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Sir:


In response to the Office Action mailed January 20, 2004, applicants propose to amend Fig. 6 to add blocks 66 and 68. With the Examiner's approval, the changes will be incorporated into the formal drawings upon indication of allowance.

If there are any questions regarding the present application, the Examiner is invited to contact the undersigned attorney at the telephone number listed below.

Respectfully submitted,

April 20, 2004

Date


Paul E. Steiner
Reg. No. 41,326
(703) 633 - 6830

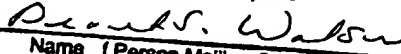
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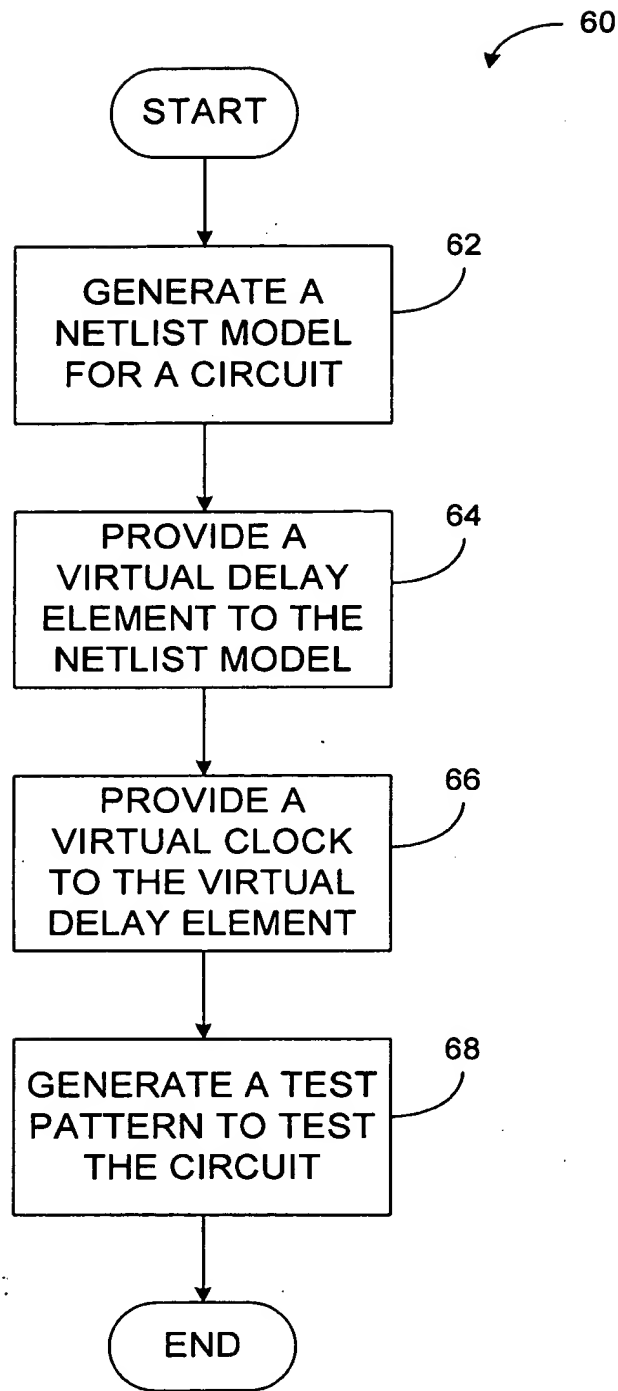


Figure 6

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